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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,446	11/28/2003	Hyang-Kyun Oh O2MICRO 99.06 CON DIV		1222
32047	7590 07/30/2004	EXAMINER		
GROSSMAN, TUCKER, PERREAULT & PFLEGER, PLLC			KIM, PAUL L	
55 SOUTH COMMERICAL STREET MANCHESTER, NH 03101		ART UNIT	PAPER NUMBER	
			2857	
		DATE MAILED: 07/30/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	-
Office Action Summer:	10/724,446	OH ET AL.	
Office Action Summary	Examiner	Art Unit	
	Paul L Kim	2857	
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tire within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 28 No			ø
·	action is non-final.		
3) Since this application is in condition for allowar closed in accordance with the practice under E	· · · · · · · · · · · · · · · · · · ·		
Disposition of Claims			
<ul> <li>4)  Claim(s) 21-33 is/are pending in the application 4a) Of the above claim(s) is/are withdraw</li> <li>5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 21-33 is/are rejected.</li> <li>7)  Claim(s) is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or</li> </ul>	vn from consideration.		
Application Papers			
9)☐ The specification is objected to by the Examine 10)☐ The drawing(s) filed on is/are: a)☐ acce		Examiner.	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Ex		•	
Priority under 35 U.S.C. § 119			
a) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of:  1. ☐ Certified copies of the priority documents 2. ☐ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicat ity documents have been receive I (PCT Rule 17.2(a)).	ion No ed in this National Stage	
Attachment(s)  1)	4) ☐ Interview Summary		
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail D 5)  Notice of Informal F 6)  Other:	ate Patent Application (PTO-152)	

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#### **DETAILED ACTION**

### Claim Objections

1. Claim 29 is objected to because of the following informalities: The term "convention" should be –conventional--. Appropriate correction is required.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 21-33 are rejected under 35 U.S.C. 102(e) as being anticipated by Huang et al.

With regard to claim 21, Huang et al teaches a method for detecting a plurality of expansion cards comprising: detecting that a card is inserted into a slot (fig. 3, part 30a), determining the type of card using PC Card signal lines (fig. 4, part 30a), enabling smart card reader logic or PC Card logic when the type of card is determined (fig. 4, parts 52 & 54), and enabling MUX logic to provide communication between the card and bus controller logic using PC Card protocols (fig. 3, part 32).

With regard to claim 22, Huang et al teaches the step of determining the type of card comprising the steps of: determining the signal state of a first and second card

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detection signal lines, determining the signal state of a first and second voltage select signal lines, determining if the first and/or second card detection signal lines, or the first and/or second voltage select signal lines, comprise a signal state that is reserved by a PC Card signal specification, determining the signal state of a PC Card signal line that is unused during the detection of a PC Card; and determining the presence of an expansion card that complies with the PC Card Specification and an expansion card that complies with a specification other than the PC Card Specification based on the signal states of the first and second card detection signal lines, and/or the first and/or the second voltage select signal lines, and/or the unused PC Card signal line (col. 3, lines 35+).

With regard to claim 23, Huang et al teaches interfacing the card to a bus using the controller logic to provide communication between the bus and the card (fig. 2, part 10).

With regard to claim 24, Huang et al teaches a system for the detection of a plurality of expansion cards comprising: a first socket for receiving a first expansion card that complies with the PC Card Specification (fig. 3, part 12), a second socket for receiving a second card that complies with a specification other than the PC Card (fig. 3, part 14), an integrated controller comprising first logic sets for detecting the first expansion card (fig. 3, part 30a), second logic sets for detecting and operating the second expansion card (fig. 3, part 30b), MUX logic enabled by the first and second logic sets to provide communication between the first and second expansion card and a bus controller logic using PC card communication protocols (fig. 3, part 38).

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With regard to claim 25, Huang et al teaches the first card comprising a Card Bus card (fig. 4, part 52).

With regard to claim 26, Huang et al teaches the second card comprising a Smart Card (col. 5, lines 11-12).

With regard to claim 27, Huang et al teaches the integrated controller further comprising a bus interface to bus controller to communicate with a bus (fig. 2, part 10).

With regard to claim 28, Huang et al teaches the bus comprising a PCI bus and conventional PC card communication protocols (fig. 4, part 38).

With regard to claim 29, Huang et al teaches the second logic set detects the second card using conventional PC card signal lines (fig. 4, part 32).

With regard to claim 30, Huang et al teaches an integrated controller comprising: first logic sets for detecting and operating a first expansion card (fig. 3, part 12), second logic sets for detecting and operating a second expansion card (fig. 3, part 14), and a bus interface controller to provide communication between the first and second expansion card and a bus interface using conventional PC card communication protocols (fig. 3, part 38).

With regard to claims 31 and 32, Huang et al teaches one of the expansion cards comprising a PCMCIA card (col. 4, lines 63-67 & col. 5, lines 11-12).

With regard to claim 33, Huang et al teaches the PC Card protocols comprising PCMCIA protocols (col. 16, lines 16-20).

#### Conclusion

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4. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure. Korowitz et al teaches an apparatus that controls multiple PC

card devices on a same bus. Hamann et al teaches a system that accesses different

types of cards, such as PC cards and smart cards, on a same bus. Tachibana et al,

Pearce et al, and Verseput et al all teach an apparatus for operating more than one PC

card slot. Potdevin et al teaches a computer interface that can read both a PC card and

a smart card.

5. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Paul Kim whose telephone number is 571-272-2217.

The examiner can normally be reached on Monday-Thursday 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Marc Hoff can be reached on 571-272-2216. The fax phone numbers for

the organization where this application or proceeding is assigned are 703-872-9306 for

regular communications and for After Final communications.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the receptionist whose telephone number is 703-308-

0956.

PK July 25, 2004 SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

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